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Patent Application

Inventor(s): Kelly, et al.

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Examiner: THAI, TUAN V

Application No.: 09/699,947

Title: TRANSLATED MEMORY PROTECTION APPARATUS FOR AN ADVANCED MICROPROCESSOR

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
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---	E	JP6266553	09/22/1994	JPO	G06F	9/30	---	X
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Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
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Th.	R	"PARALLEL PROCESSING MECHANISM" compiled by Yoshizo Takahashi, First edition published on 08/25/1989 by Maruzen (pp. 186-199)
Examiner	Tuan Thai	
Date Considered	05/09/07	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.